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10/3/01

jc997 U.S. PTO

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Inventor: Shane J. Trapp

Title: Method of Forming Integrated Circuitry and Method of Forming Shallow  
Trench Isolation in a Semiconductor Substrate

Assignee: Micron Technology, Inc.

**INFORMATION DISCLOSURE STATEMENT**

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with  
37 CFR §1.56. Copies of documents listed on the Form PTO-1449 are  
enclosed, except that copies of the listed pending applications are not enclosed.

No admission is made regarding whether all the submitted references are  
prior art.

Respectfully submitted,

Dated: 7/25/01

Inventor: Shane J. Trapp  
Shane J. Trapp

Dated: 8-1-01

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